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Correspondence Information

Correspondence Customer Number:: 20350

Application Information

Title Line One::	VLIW Computer Processing Architecture
Title Line Two::	with On-chip DRAM Usable as Physical
Title Line Three::	Memory or Cache Memory
Total Drawing Sheets::	4
Formal Drawings?::	No

Application Type:: Utility
Docket Number:: 16747009910
Secrecy Order in Patent Appl.?:: No

Continuity Information

This application is a:: Non-Prov. of Provisional
> Application One:: 60/187,687
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